

We are presenting the fabrication of nanometric resonators, from a new approach, based on ion implantation by a focused ion beam (FIB). This new method allows the fabrication of functional suspended nanodevices, from the electrical and mechanical point of view, without using any resist.[1] This method is i) fast and simple, where only three steps are needed; ii) high resolution, it is demonstrated the fabrication of 4 μm length and 10 nm diameter suspended devices (fig 1a); iii) flexible, it is feasible the definition of structures of different shape (fig 1b); iv) reproducible (fig 1c) and v) CMOS compatible.

The principal results can be classified as:

a) Investigation of the effect of gallium ion implantation onto silicon from the process and nanoelectromechanical material properties point of view.[1,2]

b) Development and optimization of the fabrication process, especially controlling the dimensions and the combination with other fabrication processes. That method permits to obtain customized devices. It is a versatile prototyping method that allows the fabrication of small batches of devices of nanometric dimensions that can be employed for the scientific and academic experimentation.[2]

c) Investigation of the electrical, mechanical and electromechanical properties [3] of the devices, specifically suspended silicon nanowires (SiNWs) that can be employed as high frequency mechanical resonators or single hole transistors (SHoTs). We investigated and fabricated ultra-thin field effect transistors (10 ~ 15 nm) and suspended transistors (fig 1d) that exhibits Coulomb blockade electrical characteristics at low temperature thanks to the nanocrystals that are grown during the high temperature fabrication step.[4]

References

[1] Llobet J, Sansa M, Gerbolés M, Mestres N, Arbiol J, Borrís X and Pérez-Murano F, *Nanotechnology* 25 (2014) 135302

[2] Llobet J, Gerbolés M, Sansa M, Bausells J, Borrís X and Pérez-Murano F, *J MicroNanolith MEMS MOEMS* 14 (2015) 31207

[3] Llobet J, Sansa M, Lorenzoni M, Borrís X, San Paulo A and Pérez-Murano F, *Appl. Phys. Lett.* 107 (2015) 73104

[4] Llobet J, Krali E, Wang C, Arbiol J, Jones M E, Pérez-Murano F and Durrani Z A K, *Appl. Phys. Lett.* 107 (2015) 223501

Figures

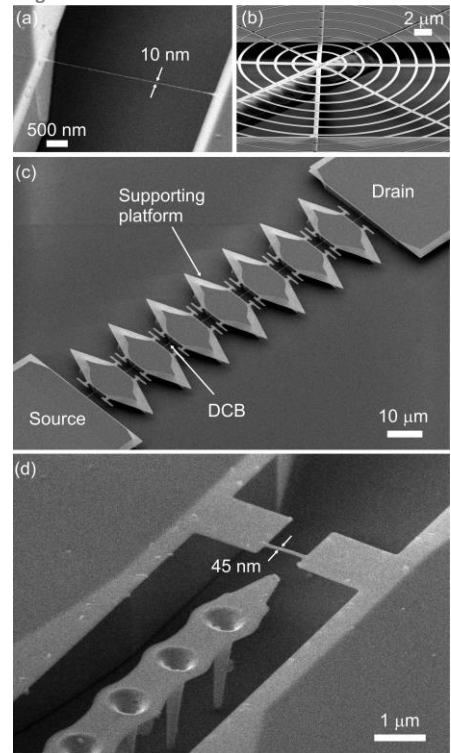


Figure 1: 45 degree SEM tilted images of different silicon suspended devices fabricated by the approach presented. (a) SEM image of a 4.3 μm length and 10 nm diameter suspended SiNW. (b) SEM image of a suspended Si spider-web that consists on concentric circular wires supported by straight wires. (c) Array of eight suspended Si doubly clamped beams (DCBs) configured in series and supported by the source and drain electrodes and seven supporting platforms. (d) Suspended SHoT.